

GUEST OF HONORS

CHIEF PATRONS

Thiru.R.Santhossh

Correspondent
CIT Institutions, Coimbatore.

Sri Vishnu Nischal Rajkumar

Director Admissions, CIT
Institutions, Coimbatore.

PATRONS

Dr.A.Rajeswari

Principal,
Coimbatore Institute of
Technology, Coimbatore.

Dr.N.R.Alamelu

Chief Academic Officer,
CIT Institutions,
Coimbatore.

CONVENER

Dr.M.Poongothai,

Professor and Head, Department of ECE, CIT

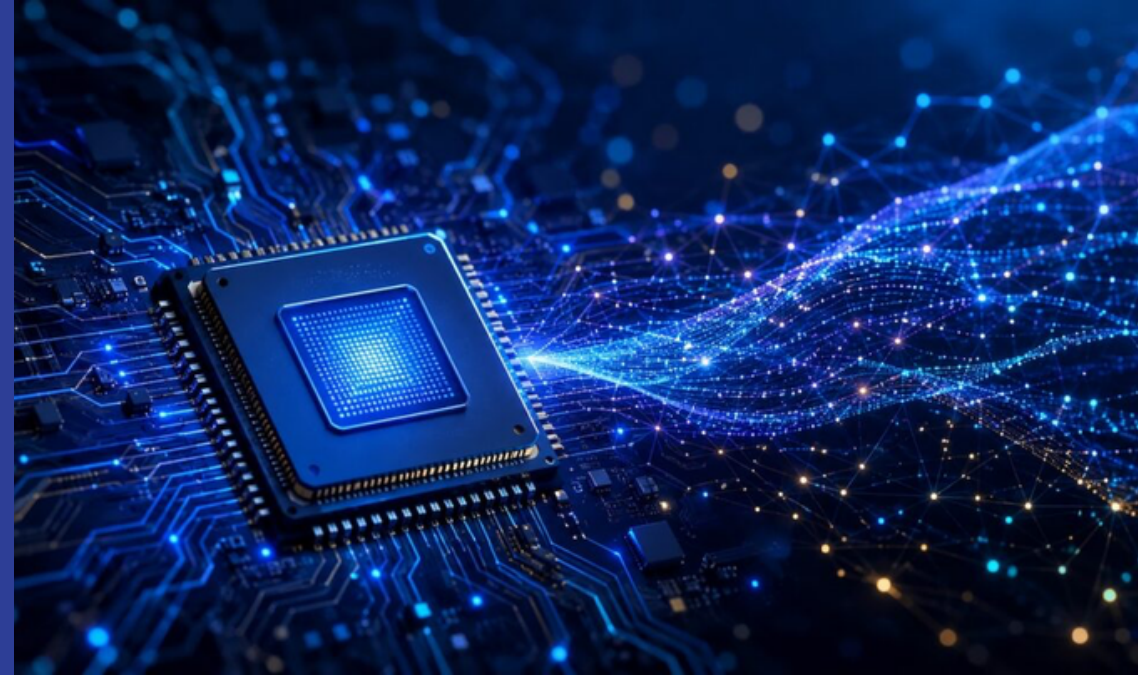
Faculty Coordinators:

Dr.I.S.Akila, Assistant Professor, Department of ECE, CIT

Dr.D.Diana Josephine, Assistant Professor, Department of ECE, CIT

Ms.S.Dhanalakshmi, Assistant Professor, Department of ECE, CIT.

*Exploring the Technology Continuum from
Silicon to AI*



COIMBATORE INSTITUTE OF
TECHNOLOGY

Department of Electronics and Communication
Engineering

Industry Conclave- TechConnect 2026

From Silicon to AI: Enabling the Future
of Intelligent Networking



28.08 2026



LBCH

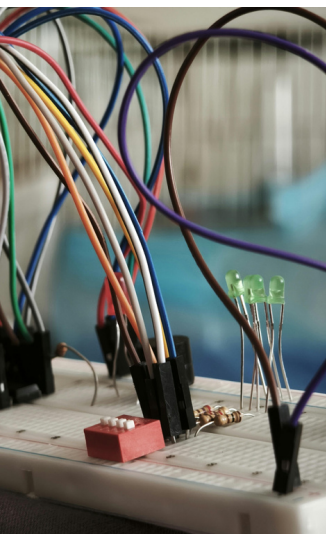


9.15 am - 4:45 pm

About CIT

The V. Rangasamy Naidu Educational Trust devoted to the cause of promoting technical and scientific literacy, established Coimbatore Institute of Technology (CIT) in Coimbatore, Tamil Nadu, South India in 1956. CIT is one of the most reputed and prestigious educational institutions in India. The Institute backed by World Class research and development over the years attained autonomous status in 1987.

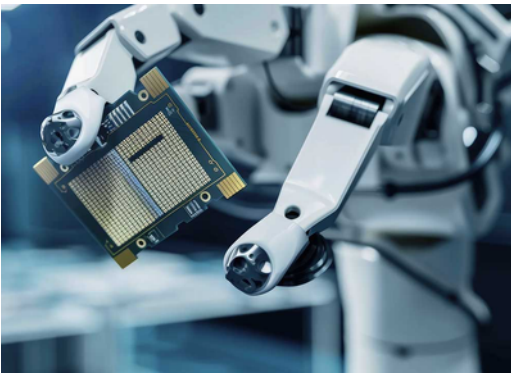
About the Department



The Department of ECE was started in the year 1968. The department offers UG– B.E Electronics and Communication Engineering, B.E Electronics Engineering (VLSI Design and Technology) and Research (Full Time and Part Time) programmes. The department has dynamic and vibrant faculty specialized in various fields of Electronics and Communication Engineering with proven research experience in niche areas. The department has produced 33 doctorates in the area of Signal processing, Wireless Communication, Networking, Wireless sensor Networks, Image processing, VLSI Design and Embedded systems.

About the conclave

The conclave aims to bring together leading experts from the semiconductor, communication, networking, and artificial intelligence industries to discuss the technologies driving the next generation of intelligent systems. It serves as a platform to strengthen academia–industry collaboration, inspire innovation among students and researchers.



EVENT SCHEDULE



Mr.Sriram Balasubramanian

Executive Director,
Synopsys- Bengaluru, India.

TITLE: How AI is Transforming Chip Design and Shaping the Future of VLSI Careers

Time:10:00 -11:15 pm



Mr. Vinothkanna Chockanathan

Staff Engineer, Advanced Packaging Professional,
Sandisk, Bengaluru, India.

TITLE: From Moore's Law to the AI Era: Why Memory and Packaging Matter More Than Ever

Time: 11:30 - 12:45 pm



Mr. Senthil Kumar Balasubramanian

Senior Director, Global Wi-Fi Software Lead,
Netgear Technologies India Pvt Ltd, Chennai, India

TITLE: From silicon to AI: inside a Wi-Fi 7 access point

Time: 2:00 - 3:15 pm



Mr. Bharath Ram Vivek N

Manager / BHEL Site Office,
ICF-Chennai, India.

TITLE: Networks in Trains – From Relay Logic to AI-Driven Intelligent Networks

Time: 3:15 - 4:30 pm