



COIMBATORE INSTITUTE OF TECHNOLOGY

(AUTONOMOUS INSTITUTION)

COIMBATORE - 641 014 ♦ TAMIL NADU ♦ INDIA

CORRESPONDENT

Dr. S.R.K. PRASAD D.Sc.,(USA)

PRINCIPAL

Dr. V. SELLADURAI M.E., Ph.D.,
FIE, FIIIE, FIPE, Sr.M(ORSI),LM(SSI)

13.08.2020

Ref :

Date :

C/10A

CIRCULAR

WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

III, V, VII Semester B.E / B.Tech., III Semester M.E.,
III, V, VII & IX Semester M.Sc.,(S.S), M.Sc.,(D.S), III, V, VII Semester
M.Sc.,(DCS), III Semester M.Sc.,(AIML),
III, V Semester MCA and III, V & VII Semester Part time B.E.

Date of Reopening	:	06.08.2020
Last Working Day	:	30.10.2020
First Mid-Semester Examinations	:	Third week of September 2020
Second Mid-Semester Examinations	:	Last week of October 2020

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

Students are advised to be regular in attending the classes, in writing the Mid-Semester Examinations and in submission of assignments etc.

The following Saturdays are declared as working days with the time table mentioned

Date	Time table - Day	Date	Time table - Day
29.08.2020	Monday	26.09.2020	Friday
05.09.2020	Tuesday	03.10.2020	Monday
12.09.2020	Wednesday	10.10.2020	Tuesday
19.09.2020	Thursday	17.10.2020	Wednesday
		24.10.2020	Thursday

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24.10.2020

C/19

CIRCULAR

REVISED WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

III, V, VII Semester B.E / B.Tech., III Semester M.E.,
III, V, VII & IX Semester M.Sc.,(S.S), M.Sc.,(D.S), III, V, VII Semester
M.Sc.,(DCS), III Semester M.Sc.,(AIML),
III, V Semester MCA and III, V & VII Semester Part time B.E.

Date of Reopening	:	06.08.2020
Last Working Day	:	07.11.2020
First Mid-Semester Examinations	:	Third week of September 2020
Second Mid-Semester Examinations	:	First week of November 2020

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments etc.,

Students are advised to be regular in attending the classes, in writing the Mid-Semester Examinations and in submission of assignments etc.

The following Saturdays are declared as working days with the time table mentioned

Date	Time table Day
31.10.2020	Friday
07.11.2020	Monday

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18.12.2020
FIE, FIF, FIF, FORSI, LM(ISSE)

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WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

First Semester M.Sc., (Software Systems, Data Science, Decision and Computing Sciences and Artificial Intelligence and Machine Learning)

Date of Opening	:	23.09.2020
Last Working Day	:	22.01.2021
First Mid-Semester Examinations	:	First week of December 2020
Second Mid-Semester Examinations	:	Third week of January 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

Students are advised to be regular in attending the classes, in writing the Mid-Semester Examinations and in submission of assignments etc.

The following Saturdays are declared as working days with the time table mentioned.

Date	Time table Day	Date	Time table Day
05.12.2020	Monday	26.12.2020	Thursday
12.12.2020	Tuesday	02.01.2021	Friday
19.12.2020	Wednesday	09.01.2021	Monday

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09.12.2020

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WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

First Semester B.E / B.Tech.,

Date of Reopening	:	16.11.2020
Last Working Day	:	28.02.2021
First Mid-Semester Examinations	:	Third week of January 2021
Second Mid-Semester Examinations	:	Last week of February 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

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The following Saturdays are declared as working days with the time table mentioned

Date	Time table Day	Date	Time table Day
05.12.2020	Monday	23.01.2021	Tuesday
12.12.2020	Tuesday	30.01.2021	Wednesday
19.12.2020	Wednesday	06.02.2021	Thursday
26.12.2020	Thursday	13.02.2021	Friday
02.01.2021	Friday	20.02.2021	Monday
09.01.2021	Monday	27.02.2021	Tuesday



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20-21 - ODD.

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WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

III Semester B.E / B.Tech (Lateral Entry)

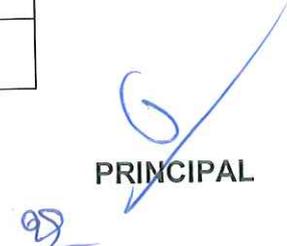
Date of Opening	:	16.11.2020
Last Working Day	:	31.12.2020
First Mid-Semester Examinations	:	Second week of December 2020
Second Mid-Semester Examinations	:	Last week of December 2020

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

Students are advised to be regular in attending the classes, in writing the Mid-Semester Examinations and in submission of assignments etc.

The following Saturdays are declared as working days with the time table mentioned

Date	Time table Day	Date	Time table Day
05.12.2020	Monday	19.12.2020	Wednesday
12.12.2020	Tuesday	26.12.2020	Thursday


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WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

First Semester Master of Computer Application

Date of Opening	:	16.11.2020
Last Working Day	:	28.02.2021
First Mid-Semester Examinations	:	Third week of January 2021
Second Mid-Semester Examinations	:	Last week of February 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

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The following Saturdays are declared as working days with the time table mentioned

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05.12.2020	Monday	23.01.2021	Tuesday
12.12.2020	Tuesday	30.01.2021	Wednesday
19.12.2020	Wednesday	06.02.2021	Thursday
26.12.2020	Thursday	13.02.2021	Friday
02.01.2021	Friday	20.02.2021	Monday
09.01.2021	Monday	27.02.2021	Tuesday

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24.12.2020

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WORK SCHEDULE FOR 2020-21 (ODD SEMESTER)

First Semester Part time BE

Date of Opening	:	16.11.2020
Last Working Day	:	28.02.2021
First Mid-Semester Examinations	:	Third week of January 2021
Second Mid-Semester Examinations	:	Last week of February 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

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18.02.2021

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WORK SCHEDULE FOR 2020-21 (~~ODD~~ SEMESTER)

I Semester M.E

Date of Reopening	:	28.12.2020
Last Working Day	:	28.04.2021
First Mid-Semester Examinations	:	Last week of February 2021
Second Mid-Semester Examinations	:	Last week of April 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

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21.01.2021
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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

VIII Semester B.E / B.Tech,

Date of Reopening	:	16.12.2020
Last Working Day	:	20.04.2021
First Mid-Semester Examinations	:	Second week of February 2021
Second Mid-Semester Examinations	:	Second week of April 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

Students are advised to be regular in attending the classes, in writing the Mid-Semester Examinations and in submission of assignments etc.


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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

VI Semester B.E / B.Tech,
IV & VI & VIII Semester M.Sc. (S.S), (D.S),(DCS)
IV Semester M.Sc.(AIML), IV & VI Semester MCA and
IV & VI Semester Part time B.E.

Date of Reopening	:	04.01.2021
Last Working Day	:	04.05.2021
First Mid-Semester Examinations	:	First week of March 2021
Second Mid-Semester Examinations	:	First week of May 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

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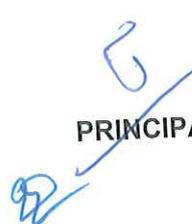
WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

IV Semester B.E / B.Tech.,

Date of Reopening	:	18.01.2021
Last Working Day	:	17.05.2021
First Mid-Semester Examinations	:	Second week of March 2021
Second Mid-Semester Examinations	:	Second week of May 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

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04.03.2021

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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

II Semester MCA

Date of Reopening	:	08.04.2021
Last Working Day	:	02.08.2021
First Mid-Semester Examinations	:	Last week of May 2021
Second Mid-Semester Examinations	:	Last week of July 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

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15.04.2021

C/73

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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

II Semester B.E /B.Tech., and II Semester Part time B.E

Date of Reopening	:	12.04.2021
Last Working Day	:	20.07.2021
First Mid-Semester Examinations	:	Last week of May 2021
Second Mid-Semester Examinations	:	Third week of July 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Assignments, Tutorials, Quiz, Seminar etc.,

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17.04.2021	Monday	05.06.2021	Tuesday
24.04.2021	Tuesday	12.06.2021	Wednesday
08.05.2021	Wednesday	19.06.2021	Thursday
15.05.2021	Thursday	26.06.2021	Friday
22.05.2021	Friday	03.07.2021	Monday
29.05.2021	Monday	10.07.2021 17.07.2021	Tuesday Wednesday


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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

II Semester M.E

Date of Reopening	:	24.05.2021
Last Working Day	:	31.08.2021
First Mid-Semester Examinations	:	Third week of July 2021
Second Mid-Semester Examinations	:	Last week of August 2021

Sessional marks will be awarded by the respective faculty members on the basis of student's performance in Mid-Semester Examinations, Attendance and Assignments.

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WORK SCHEDULE FOR 2020-21 (EVEN SEMESTER)

II Semester M.E

Date of Reopening	:	24.05.2021
Last Working Day	:	31.08.2021
First Mid-Semester Examinations	:	Third week of July 2021
Second Mid-Semester Examinations	:	Last week of August 2021

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05.06.2021	Tuesday	10.07.2021	Tuesday	14.08.2021	Tuesday
12.06.2021	Wednesday	17.07.2021	Wednesday	21.08.2021	Wednesday
19.06.2021	Thursday	24.07.2021	Thursday	28.08.2021	Thursday
26.06.2021	Friday	31.07.2021	Friday		

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**COIMBATORE INSTITUTE OF TECHNOLOGY, COIMBATORE-14
DEPARTMENT OF INFORMATION TECHNOLOGY**

19CIL46 - DATABASE MANAGEMENT SYSTEMS LABORATORY

Course: II BTECH IT Semester: IV AY:2020-2021

COURSE PLAN

Faculty In-charge(s):

Faculty
Dr.N.AnithaDevi
Ms.G.Thilagavathi
Ms.S.Devi

Lab Timings:

DAY	TIME
Tuesday	2.00PM to 4.45 PM
Thursday	9.55AM to 12.55 AM

Course Pre-requisites: NIL

Course Learning Objectives:

To gain practical knowledge on implementation of sql queries and pl/sql programs.

Nature of the Course: Group 5 - Practical

Expected Level of Output : Conceptual And Design Level

Continuous Assessment (CA) : 100 Marks

Expected outcome of the Course:

CO1	Practice various DDL, DML, TCL commands to perform various database operations for the given applications.	[AP][A]
CO2	Demonstrate open database connectivity by establishing connections between front end and databases	[AP][A]
CO3	Experiment various PL/SQL features such as procedures, functions, triggers and report generation.	[AP][A]

LIST OF EXPERIMENTS

WEEK	TOPICS	CO MAPPING	BLOOMS TAXONOMY LEVEL
1	Database creation, insertion and deletion.	CO1	AP,A
2.	Queries based on DML commands.	CO1	AP
3.	Aggregate functions.	CO1	AP
4.	Sub-queries and joins.	CO1	A
5.	Group by clause and DATE functions.	CO1	A
6.	Complex queries using joins	CO1	AP
7.	CAT-I		
8	Views and Triggers.	CO1,CO3	A
9.	PL/SQL Basics	CO1,CO3	A
10.	PL/SQL Procedures.	CO1,CO3	AP
11.	PL/SQL Functions	CO1,CO3	A
12.	Using Cursors and functions in PL/SQL blocks	CO1,CO3	A
13.	ODBC Connectivity	CO1,CO2	C
14.	CAT-II		
15.	Applications using ODBC Connectivity	CO1,CO2	C

Bloom's Legends:

R-Remembering

U-Understanding

AP-Applying

A-Analyzing

C-Creating

E – Evaluating

Instructions:

- Students should follow the formal dress code.
- Students should bring the observation to the lab classes.
- Performance of the students will be assessed by conducting Continuous Assessment Test (CAT) periodically.
- 75% Attendance in the lab session is mandatory for writing CAT exam.
- Two CATs will be conducted and each will be evaluated for 100 marks.
- Assignments will be given in the lab class. It should be completed within the lab class and it is evaluated for 100 marks.
- Completed record should be submitted for the previous week assignments in the beginning of the next lab session.

Continuous Assessment Method:



Course Code	Course Name	Contact Hours			
		L	T	P	C
15CIE05	Object Oriented Analysis and Design	3	0	0	3

Name of the Faculty : Dr.K.Amshakala

Time Slot : Monday – 1st Hour , Wednesday– 3rd hour , Friday – 2nd hour

Faculty Cabin Details: L-107

Academic Year : 2020-2021 (Even Semester) VI Semester Elective

1. Course pre-requisites : Basics of OOPs (UG course)

2. COURSE OUTCOME

CO1 : Describe the fundamental aspects of Object Models.

CO2 : Identify classes and Objects and establish relationship among objects and classes and objects.

CO3 : Selection and Application of appropriate object oriented design Methodologies in real world applications.

CO4 : Construct Class, Use Case and Interaction Diagrams for software specifications.

CO5 : Illustrate the object-oriented software development process and work flow.

3. Expected Level of Output : Conceptual And Design Level

4. Department Offered : Computer Science and Engineering

5. Nature of the Course : 80 % Descriptive & 20 % Assignment

Internal Assessment (CIA) : 25 Marks

Semester End Examination (SEE) : 75 Marks

Course Input :

Course Schedule

Week	Topics To Be Covered	Assignment Schedule
1.	Complexity : Structure of complex of systems, Inherent complexity of software - attributes of a complex system	Assignment - I
2.	Object Model: Evolution of object models - Foundations of Object model	
3.	Elements of Object.	
4.	CLASSES AND OBJECTS Nature of an object- Relationships among objects - Nature of class - Relationship among classes	Assignment - II
5.	Interplay of Classes and objects Building quality classes and objects	
6.	Classification: Importance of Proper Classification -Identifying classes and objects.	



7.	METHODOLOGY AND MODELING Object Oriented methodologies : Rumbaugh - Booch - Jacobson	
8.	Patterns - Frameworks	Assignment - III
9.	Unified approach, patterns.	
10.	UNIFIED MODELING LANGUAGE Diagram Taxonomy- static and dynamic models Class diagram : Notation- Object diagram - Class interface notation - Binary Association notation, Association Rule - Qualifier - Multiplicity - OR Association - Association Class - N-ary association - Aggregation and Composition - Generalization.	
11.	Use case diagram : Actors-Use Cases-Use case Diagram-Include and Extend Relationships-Generalization.	
12.	Interaction Diagrams : Sequence Diagrams - Collaboration Diagrams.	
13.	State chart diagram : Activity diagram Implementation diagrams: Component diagram * Deployment diagram.	Assignment - IV
14.	PROCESS Macro Process: Content Dimension - Time Dimension - Milestones and Phases – Iterations.	
15.	Micro Process : The analysis and Design Process - Micro process and levels of abstraction - Defining elements relationships.	Assignment - V
16.	Case Studies	

Instructions

- 75 % of attendance is mandatory to attend the mid semester exams.
- Any change in the course plan will be informed to the students at the right time.

6. Course assessment methods:

Assessment	Marks
Mid-Semester-I	7.5
Mid-Semester-II	7.5
Assignments	5
Case Study	5
Total	25

Faculty In-charge

Coimbatore Institute of Technology

(Government Aided Autonomous Institution Affiliated to Anna University Coimbatore)

Coimbatore-14

Department of Computer Science and Engineering

Academic Year : 2020-2021

Semester:III

Course/Branch : B.E Computer Science and Engineering

Subject Code/Name : 19CI32 Digital Design

Name of the Faculty : Dr.K.Amshakala

Course Plan

Course Outcomes

CO1:Understand the basics of number systems, arithmetic operations, data representation schemes.

CO :Understand the concepts of logic gates, Boolean algebra and simplify the Boolean expressions.

CO3 :Acquire knowledge in combinational logic and to design combinational circuits.

CO4 : Ability to understand sequential logic and to design digital systems using sequential logic.

CO5:Understand the various types of Memory, Memory organizations and their operations

Course Schedule

Week	Topics To Be Covered	Test/ Assignment/Tutorial Schedule
1.	Digital Computers and digital systems. Number system- Binary, Octal, Hexadecimal, Base Conversions.	Assignment - I
2.	Digital logic circuit- Half Adder- Half Subtractor - Boolean Algebra - Basic Definitions – Theorems & Properties - Canonical and Standard Forms. Digital Logic gates.	
3.	Simplification of Boolean Functions – Karnaugh Maps.	
4.	Complements-Subtraction of (unsigned) numbers using Complements. Binary Codes - BCD, Excess-3, Gray, ASCII, Error Detecting Codes- Reflected code.	
5.	Don't Care Conditions, NAND and NOR Implementation, Quine –McCluskey method.	Assignment - II
6.	Combinational logic: Full adder, Binary parallel adder, BCD adder- Full subtractor	
7.	Code conversion. Decoders- Encoders – Multiplexer – Demultiplexer	Assignment - III
8.	Programmable Logic Arrays(PLA)- Programmable Array Logic(PAL).	

9.	Introduction - Flip Flops - Types - Triggering of Flip Flops.	
10.	State table, State diagram, State equation, State reduction assignment – Flip flop Excitation table	Assignment - IV
11.	Design Procedure - Design of Counters- Ripple Counters- Binary and BCD ripple counters.	
12.	Synchronous counters - Binary and Binary up-down counters-BCD Counters. Registers – Shift Register.	Assignment - V
13.	Memory Basics-Random Access Memory - RAM Family –Static RAMs-Memory Cell - Basic SRAM Cell Array –Cache Memory - DRAM Memory Cells – Types of DRAMs.	
14.	Read-Only Memory-ROM Family-Mask ROM-Simple ROM –Internal ROM Organization– Programmable ROMs. Flash Memory- Flash Memory Cell-Flash Memory Operation-Basic Flash Memory Array-Comparison of Flash Memories with other Memories.	
15.	Fixed point Binary Data- Sign and Radix Point Representation-Signed Binary Numbers- Arithmetic Addition and Subtraction-Overflow. Decimal Data- Floating Point Data.	

TEXT BOOKS:

1. M. Morris Mano, “Digital Logic and Computer Design”, Pearson Education, 1st edition, 2008.
2. Thomas L.Floyd, “Digital Fundamentals”, Pearson Education, 10th edition, 2011. (For “Memory”Only).

REFERENCE BOOKS:

1. Tocci R.J., Neal S. Widemer, Gregory L. Moss “Digital Systems: Principles and Applications”, Prentice Hall of India (New Delhi), 10th edition, 2009.
2. V.Rajaraman, T.Radhakrishnan, “Digital Logic and Computer Organization”, PHI Learning Pvt. Ltd, New Delhi, 2009.(For “Programmable Logic Arrays).

Evaluation of Marks

- Marks are assessed based on mid semester marks, attendance and assignment submissions in time.
- 75 % of attendance is mandatory to complete the course.
- Any change in the course plan and the evaluation scheme will be informed to the students at the right time.

Coimbatore Institute of Technology
 (Government Aided Autonomous Institution Affiliated to Anna University Coimbatore)
 Coimbatore-14
 Department of Computer Science and Engineering

Academic Year : 2020-2021 Semester: III
 Course/Branch : B.E (CSE)
 Subject Code/Name : 19CI33 - Object Oriented Programming and Java
 Name of the Faculty : Dr.K.Amshakala

19CI33 - OBJECT ORIENTED PROGRAMMING AND JAVA

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ASSESSMENT: THEORY

COURSE OUTCOMES

- CO1 : Identify classes and objects in software applications.*
- CO2 : Design objects for specific applications in java.*
- CO3: Understand the concepts of Exception Handling and its types.*
- CO4 : Demonstrate the concepts of multithreading and java networking.*
- CO5: Demonstrate the concepts of applet and its use.*

Course Schedule

Week	Topics To Be Covered	Test/ Assignment/Tutorial Schedule
1.	Introduction to object oriented languages & Object oriented programming paradigm. Basic concepts of object oriented programming - Procedural vs Object oriented programming.	Assignment - I
2.	Java and Internet - Byte Code - Features of Java - Java Development Environment	Quiz
3.	Java Programming: Methods and Classes - Constructor - Garbage Collection	
4.	Overloading - Inheritance.	
5.	Overriding	
6.	Packages and Interfaces Buffer	
7.	Java IO systems - String Handling : String and String	
8.	Exception Handling: Fundamentals of Exception handling and types - Built in Exceptions - User defined Exceptions.	Assignment - II
9.	Multithreaded Programming : Thread Model - The Main Thread – Creating a Thread	
10.	– Creating Multiple Threads - Thread priorities	Assignment - III

