

SPONSORING CERTIFICATE

This is to certify that Dr./Mr./Ms
.....of
..... department is
being sponsored for attending the AICTE-QIP
Sponsored One Week STC on "Power Quality Issues
and its Mitigation in Smart/Micro Grid Systems"
organized by Department of EEE, Coimbatore
Institute of Technology during 17th - 23rd November
2014 if selected. The college is approved by AICTE.

Date : Signature of the Head of the Institution
(With seal)

ORGANIZING COMMITTEE

Chief Patron
Dr. S.R.K. PRASAD,
Correspondent, CIT, Coimbatore

Patron
Dr. R. PRABHAKAR,
Secretary, CIT, Coimbatore

President
Dr. V. SELLADURAI,
Principal, CIT, Coimbatore

Convener
Dr. S. VASANTHARATHNA,
Professor and Head, Department of EEE, CIT

Coordinators
Dr. E. CHANDIRA SEKARAN,
Associate Professor, Department of EEE, CIT
Er. S.R. SIVARASU,
Assistant Professor, Department of EEE, CIT

TARGET AUDIENCE

This one week short term course is deliberately
designed for the professionals working in:

- Faculty from Academic Institutions / Full
Time Research Scholars
- Research and Practicing Engineers
- Public/Private Industries & R&D Establishments

REGISTRATION DETAILS

No registration fee for the faculty members
working in AICTE approved colleges and
Universities. The registration fee for the participants
from Industries and R&D establishments is
Rs.3000/- per participant (To be submitted in the
form of demand draft in favour of "QIP-PQIMSMG").
The interested participants are requested to send
the filled in registration form duly signed by the Head
of the Institution to the address mentioned below on
or before the last date.

TRAVEL ALLOWNACE

The selected outstation participants from
academic institutions are eligible for 3-Tier AC train
fare by the shortest route with proper proof.

BOARDING AND LODGING

Free Boarding and lodging facilities will be
provided for the participants from AICTE approved
institutions on twin sharing basis in the College
Hostel/Guest house. No family accommodation will
be provided.

RESOURCE PERSONS

Faculty experts from premier academic
institutions and Invited experts from leading
industries, research and development organizations.

IMPORTANT DATES :

Last date for registration	10 Nov. 2014
E-mail Confirmation	12 Nov. 2014

Registration form be sent to :

Dr. E. CHANDIRA SEKARAN,

Associate Professor,

Department of Electrical and Electronics Engineering,

COIMBATORE INSTITUTE OF TECHNOLOGY,

COIMBATORE 641 014. TAMIL NADU, INDIA.

Mobile : 96292 83060, 99420 29372, 94420 03930

E-mail : citeegetpp@gmail.com

AICTE - QIP SPONSORED
ONE WEEK SHORT TERM COURSE
ON

POWER QUALITY ISSUES AND ITS MITIGATION IN SMART / MICRO GRID SYSTEMS (PQIMSMG)



17th - 23rd NOVEMBER 2014



COORDINATORS

Dr. E. CHANDIRA SEKARAN

Er. S.R. SIVARASU

Organized by

Department of Electrical and Electronics Engineering

COIMBATORE INSTITUTE OF TECHNOLOGY

(A Government Aided Autonomous Institution)

Civil Aerodrome Post, Coimbatore,

Tamilnadu, India - 641 014.

Phone : +91 - 422 - 2574071, 2574072

Fax : + 91 - 422 - 2575020

HOST INSTITUTION

Coimbatore Institute of Technology (CIT) started in 1956 by Sri.V. Rangasamy Naidu is reckoned for its academic excellence in engineering and technology. CIT is a Government Aided Autonomous Institution affiliated to Anna University, Chennai. The institute has a reputation with service of competent, well qualified faculty and dynamic management to set highest standards in engineering research and development. CIT offers under graduate, post graduate and PhD programs with a global standard curriculum promoting the students to compete internationally. The institute has celebrated its Golden Jubilee in the year 2006 and all the courses are accredited by NBA. The institute has collaborated with leading frontier universities and industries in India and abroad for the promotion of innovative research and development. CIT has been awarded with the "Bizz-2012" the world business leader award and also honored with the Outstanding Engineering Institute (SOUTH) for consecutive seventh time awarded by ABP NEWS National Education Awards.

HOST DEPARTMENT

The Department of Electrical and Electronics Engineering is one of the earliest departments in CIT which offers both under graduate, post graduate and Ph. D programmes. The department has a team of well experienced faculty members having vast experiences in various engineering and technology with specialization in the areas like Power Electronics, Power Quality, Energy Engineering, Power Systems, Control Systems, Embedded Systems, VLSI and Wireless communications. The faculty members and students of the department are dedicated to work in the current needs of the industry and have the ability to improve their expertise and incorporating the new technology to meet the emerging industrial challenges. The main objective of the department is to create a highly skilled industry ready workforce do deal with the technical challenges faced by the industries.

PREAMBLE

Globally the power system networks are undergoing tremendous restructuring due to conventional operating technologies and obsolete infrastructure. In order to meet today's growing technology and challenging demand, it is necessary to develop an intelligent power system with advanced sensing, two way communication and modern control technologies to distribute the electricity more effectively, economically and securely which leads to the concept of Smart/Micro grid. The concept of Micro Grid/ Smart Grid includes integration of renewable energy sources, distributed generation and recent Information and Communication Technology (ICT) that ensures high reliability, increased efficiency and good power quality. Smart/Micro grids are usually more sensitive for power quality problems than the regular utility grid. Fluctuating voltage level causes flicker, large amount of single phase loads connected in the three phase system creates voltage unbalance and introduction of harmonic distortion due to the presence of power electronic loads are the major causes for power quality aspects. Ensuring good power quality guarantees the compatibility between all equipments efficient and reliable operation of smart/micro grid.

This one week QIP short term course provides a discussion forum to study the power quality issues on the smart and micro grid, research challenges in PQ issues, impact on renewable energy penetration in micro grids, and advances in PQ mitigation techniques.

DISCUSSION TOPICS

- Power Quality - Introduction
- Micro / Smart Grid Architectures
- Resource Assessment and Modeling of RES
- Micro Grid Modeling and Energy Transfer
- Role of Power Conditioning Units
- Modeling and Simulation of Smart Grid Systems
- Hands on Training on PQ Measurements
- Power Quality Mitigation Methods
- Demonstration on Micro Grid System
- Smart Grid and Smart City - Evolution and Implementation
- Grid Codes and Standards

REGISTRATION FORM

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POWER QUALITY ISSUES AND ITS MITIGATION IN SMART / MICRO GRID SYSTEMS (PQIMSMG)

17th - 23rd NOVEMBER 2014

1. Name : _____

2. Organization : _____

3. Designation : _____

4. Professional Category (Please tick) :

Industry Academic Research

5. Communication Address: _____

6. Phone (O) _____ (R) _____

7. Mobile: _____

8. E-mail : _____

9. Mode of Payment*

DD No. _____ Dated _____

Bank Name : _____

Amount : _____

10. Accomodation Required : YES / NO

DD to be drawn in favor of "QIP-PQIMSMG" payable at
"Coimbatore"

Signature of the Applicant

*Only for the participants from Industries and R&D establishments