REGISTRATION FORM

AICTE Sponsored QIP - Short Term Course
on
Research Challenges in VLSI Design and Testing

NAME: _____

DESIGNATION: _____

ORGANISATION: _____
_____

MAILING ADDRESS:
_____
_____
_____

Email ID: _____

Contact No: _____

ACCOMMODATION REQUIRED: YES/NO
SEX (For Accommodation):   Male: ☐
    Female: ☐

DETAILS OF ENCLOSED DEMAND DRAFT:
DD No: _____
DATED: _____
AMOUNT: Rs._____
BANK: _____

Date: Signature

ORGANIZING COMMITTEE

Chief Patron:
Dr. S. R. K. PRASAD
Correspondent, CIT
President:
Dr. R. PRABHAKAR
Secretary, CIT
Patron:
Dr. V. SELLADURAI
Principal, CIT
Chairman:
Dr. A. RAJESWARI
Professor& Head
ECE Department, CIT
Coordinator:
Dr. S. UMA MAHESWARI
Associate Professor
ECE Department, CIT
Co-coordinators:
Ms. T. SHUNBAGA PRADEEPA
Ms. B. PREMALATHA
Assistant Professors
ECE Department, CIT

For Registration and Details
Please contact:
Ms. T. Shunbaga Pradeepa
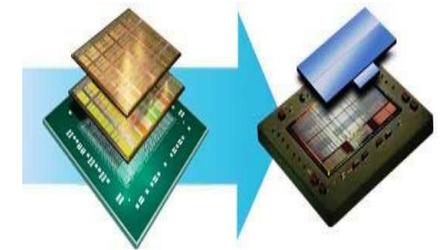Assistant Professor,
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AICTE Sponsored
QIP - Short Term Course
on
Research Challenges in VLSI Design and Testing
December 08 - 14, 2014

Organized by
Department of Electronics and Communication Engineering
Coimbatore Institute of Technology
Coimbatore –14.
(Government Aided Autonomous Institution)

TEL: 0422-2574071, 0422-2574072
FAX: 0422-2575020
Website: www.cit.edu.in

ABOUT THE COLLEGE

The V. Rangasamy Naidu Educational Trust devoted to the cause of promoting Technical and Scientific Literacy, established Coimbatore Institute of Technology (CIT) in 1956. CIT is one of the most reputed and prestigious educational Institutions in South India. The Institute endorsed by world class research and development attained autonomous status in 1987 and is affiliated to Anna University, Chennai. The Institute features strong Academic-Industrial Interaction and a high quality of research and consultancy. CIT, managed by a pedigreed lineage for the past 57 years, enjoys international repute. The Institute has the services of competent and qualified faculty, and the guidance of a visionary management to enhance the quality of education at all levels and maintains its pre-eminent position in emerging globalization.

ABOUT THE DEPARTMENT

The department of Electronics & Communication Engineering was established in the year 1968. It has been very well equipped with highly commendable facilities and is effectively guided by a set of devoted and diligent staff members. The department offers Under Graduate Program in ECE and Post Graduate programs in Communication Engineering and VLSI Design. The Department has also successfully completed many projects with the support of grants received from various funding agencies like UGC, DST, and AICTE etc.

ABOUT THE COURSE

The EDA for electronics has rapidly risen in tune with unrelenting down-scaling of the semiconductor technology. The objective of this one-week Short Term Course is to provide a high-quality forum for faculty members from

Academia to learn the innovations in Analog and Digital VLSI Design, VLSI testing and to exchange Information regarding advancements in the field of VLSI Design. The program will also enable researchers to identify the recent research topics and industrial problems to define the future of VLSI Technology.

RESOURCE PERSONS

Eminent Professors and Researchers from reputed Academic and Research Institutions and Practicing Engineers from leading industries.

ELIGIBILITY

The teachers of the AICTE approved Engineering Colleges are eligible under this scheme. Faculty members from the streams of ECE, EEE, CSE & IT can apply under this program. Candidates doing research in the field of VLSI Technologies are also eligible.

COURSE CONTENT

- Analog VLSI Design
- Digital VLSI Design
- VLSI Testing
- Reconfigurable Computing
- FPGAs
- System-On-Chip

SELECTION

Total number of participants is restricted to 30 and will be based on first come first served basis. The selected candidates will be intimated by e-mail.

IMPORTANT DATES

Last date for receiving completed registration forms: **26th November, 2014.**
Intimation to selected candidates:
03rd December, 2014.

APPLICATION PROCEDURE

QIP Candidates:

Application in the attached form should be sent to the coordinator with a caution deposit of Rs. 300/- in the form of a Demand Draft favoring "Principal CIT", payable at Coimbatore. The fee will be refunded to the participants from AICTE accredited institutions.

The QIP participants will be paid TA/DA by A/C three tier for attending the course. The DA will be paid as per rules, adjusted against boarding and lodging at Coimbatore.

Non-QIP participants can join the course by paying the following fees. Accommodation cannot be provided for Non-QIP candidates.

PARTICIPANTS REGISTRATION FEE:

Industry/R&D Organizations: Rs. 4000/-

Academic Institutions: Rs. 2000/-
(Non-QIP candidates)

The decision regarding the acceptance for the course will be taken by the coordinator after receiving the completed application form and the demand draft.

MAILING ADDRESS:

The Coordinator,
QIP-STC on
Research Challenges in VLSI Design and Testing (RCVLSIDT)
Department of Electronics and Communication Engineering
Coimbatore Institute of Technology,
Civil Aerodrome Post
Coimbatore - 641014.