

Coimbatore Institute of Technology

Department of ECE

Minutes of BOS Meeting held on 22/7/2023

Minutes of Board of Studies meeting of Electronics and Communication Engineering branch held on 22/7/2023 at 9.30 AM in M103 to discuss about the Verticals for the students admitted during 2021-2022 and onwards, curriculum and syllabi for I semester to VIII Semesters of BE ECE for the students admitted during 2023-2024 and onwards and the department vision, mission, PEOs, POs, PSOs. The following members were present during the meeting.

Principal and Professor  
- Dr. A. Rajeswari

*Rajm*  
22/7/2023

Chairman

Dr. S. Uma Maheswari

*Sa Umah*  
22/7/2023

External expert members

1) Dr. C. Saxonija

2) Dr. J. Ramesh

3) Dr. G. Laxshmi Narayanan

4) Mr. K. Sadheeskumar

*S. Saxonija*  
22/7/23

*J. Ramesh*  
22/7/23

*G. Laxshmi Narayanan*  
22/7/23

*K. Sadheeskumar*  
22/7/23

Internal Members

1) Dr. M. Poongothai

2) Dr. J. S. Arila

*M. Poongothai*  
22/7/23

*J. S. Arila*  
22/7/23

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|----------------------------------|------------------------------|
| 3) Dr. T. Balakumaran            | Berg                         |
| 4) Dr. G. Puvaneswari            | G. Puvaneswari               |
| 5) Dr. B. Premalatha             | B. Premalatha                |
| 6) Mrs. R. Kalaiyandhi           | R. Kalaiyandhi 22/7/23       |
| 7) Dr. G. Christena              | G. Christena                 |
| 8) Mrs. M. Ramya                 | M. Ramya 22/7/23             |
| 9) Dr. B. Bhuvaneshwari          | B. Bhuvaneshwari 22/7/23     |
| 10) Mrs. K. M. Priya             | K. M. Priya                  |
| 11) Mrs. T. Yathavi              | T. Yathavi 22/7/23           |
| 12) Mrs. C. Subha Priyadharshini | C. Subha Priyadharshini      |
| 13) Dr. P. Muthu Subramanian     | P. Muthu Subramanian 22/7/23 |
| 14) Mrs. Cynthia Joseph          | Cynthia Joseph 22/7/23       |
| 15) Dr. N. Deepa                 | N. Deepa                     |
| 16) Mrs. K. Harini               | K. Harini                    |
| 17) Mrs. D. Diana Josephine      | D. Diana Josephine           |
| 18) Mrs. V. Sowmya               | V. Sowmya                    |
| 19) Mrs. S. Dhanalakshmi         | S. Dhanalakshmi              |
| 20) Mrs. M. Abitha Thangam       | M. Abitha Thangam            |
| 21) Mrs. S. Bala Dhanalakshmi    | S. Bala Dhanalakshmi         |
| 22) Dr. P. Sathesh Kumar         | P. Sathesh Kumar             |
| 23) Mrs. V. Gowripriya           | V. Gowripriya                |
| 24) Mrs. R. Nithyalakshmi        | R. Nithyalakshmi             |
| 25) Ms. K. Muralidharan          | K. Muralidharan              |
| 26) Dr. S. Jayasri               | S. Jayasri                   |
| 27) Dr. P. Rupa                  | P. Rupa                      |
| 28) Dr. P. Balraj                | P. Balraj                    |
| 29) Dr. E. Justin Reuben         | E. Justin Reuben             |

The following members expressed their inconvenience to attend the meeting

- 1) Mr. Bharathi Athinayagan

2) Dr. S. Uma

Minutes of the BOS Meeting held on 22.07.2023 at 9.30 AM in M103 for B-E Electronics and communication Engineering branch. The following are the recommendations.

- 1) Student Induction Program should be three weeks.
- 2) To include virtual instrumentation topic in Electrical Engineering and Instrumentation.
- 3) To include Semiconductor Physics instead of Material science in II semester.
- 4) To reduce the content of Environmental Science.
- 5) To add simulation experiments in Devices and Circuits laboratory.
- 6) To reduce the content of Electromagnetic Fields and Waveguides.
- 7) To include topics on pulsed mode and fundamental mode asynchronous design, memory and logic arrays in Digital Electronics.
- 8) To include shift register experiments, more sequential circuits based experiments and simulation experiments in Digital Electronics laboratory.
- 9) To reduce the content of Antennas and wave propagation.
- 10) To reframe the title of the third unit of Antennas and wave propagation as Smart Antenna instead of Modern

pointed antenna.

- 11) To include Sigma Delta modulation topic in Principles of Communication.
- 12) To include parallel processing and pipelining concepts in Microcontroller based systems.
- 13) To reframe the title of Data structures and algorithms as Data structures using C.
- 14) To add recent references in Data structures and algorithms laboratory.
- 15) To reframe the title of first and 4<sup>th</sup> unit in Control Systems.
- 16) To include simulation experiments under tutorials in Control Systems.
- 17) To reduce the content of Embedded Systems and IoT.
- 18) To include logical operations based experiments in Embedded Systems and IoT and the case studies should be open ended.
- 19) To include low power VLSI concepts, SoC concepts, GNR FET, topics in VLSI Design.
- 20) To include network security concepts in Data Communication.
- 21) To reduce the content of Data Communication.
- 22) To include laboratory or tutorial component and simulation based experiments in Image Processing.

- 23) To include microwave measurements in the second unit of Microwave and Optical Communication.
- 24) To reframe the High Frequency Systems laboratory as Microwave and optical Communication laboratory.
- 25) To re-order the subjects in all the verticals.
- 26) To reframe the title of the vertical Video and Image Processing: Building Real Systems and Applications as Image and Video Processing.
- 27) To reframe the title of the first unit of Video and Image Processing: Building Real Systems and Applications.
- 28) To remove the topics on deep learning for Image processing in unit V of Video and Image Processing: Building Real Systems and Applications.
- 29) To reframe the fifth unit of DSP architecture.
- 30) To include Speech Recognition System Applications in Speech Processing.
- 31) To reframe the title of Bio-signal Processing as Biomedical Signal Processing.
- 32) To reframe the title of the fourth unit in Bio-signal Processing based on the content.
- 33) To include wavelet filter banks topics in Multirate Systems.
- 34) To reframe the first three units

based on AI/ML in computer vision.

- 35) To reduce the content of IoT Processors.
- 36) To mention the specific processors in IoT Processors.
- 37) To add a book on System on Chip in IoT Processors.
- 38) To remove Amazon web Services for IoT in IoT based System Design.
- 39) To add topics on Energy Harvesting in Wearable Technology.
- 40) To remove Google glass in Wearable Technology.
- 41) To reduce the content of unit V in Wearable Technology.
- 42) To reframe the title of the vertical Industrial IoT and Industry 4.0 as Industrial IoT.
- 43) To reframe the first unit title of Industrial IoT and Industry 4.0 as Industrial IoT standards.
- 44) To reframe the title of the vertical smart Sensors and Transducers as Smart Sensors for IoT and to reframe the first four units.
- 45) To reframe the title of the vertical smart IoT Edge nodes and its applications as IoT Edge Computing.
- 46) To reframe the title of the fifth unit of IoT Edge nodes and its applications as IoT Application.

## Development

- 47) To change the content of Mobile App Development for IoT based on the course title.
- 48) To reframe the title of the first unit of Software Defined Radio.
- 49) To include topics on Software Defined Radio architectures in Software Defined Radio.
- 50) To add Jeffrey Ho Road book in Software Defined Radio.
- 51) To reframe the unit I title of 5G Communication Networks as Introduction to 5G or overview of 5G.
- 52) To reduce overlapping of topics in High Speed Communications Verticals.
- 53) To change the unit I content Green Concepts to Green communication in Advanced wireless communication techniques.
- 54) To check for VLSI Verticals as per Anna University.
- 55) To include Verilog HDL and Low-power Design courses in professional electives.
- 56) To add FPGA based System Design in VLSI Verticals.
- 57) To add VLSI Signal Processing in VLSI verticals as these are two courses in VLSI Testing.
- 58) To frame contents of Analog IC Design based on the book

Analog Integrated IC Design by  
Kenneth W. Martin.

5a) To include topics on marches  
based memory testing in  
Semiconductor Memory Design.

Sh. D. D. D.

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